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## BIB DATA SHEET

CONFIRMATION NO. 4305

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10/528,450	05/22/2006	438	1792	050169

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP03/11988 09/19/2003

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2002-276076 09/20/2002

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***

07/18/2006

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		JAPAN	6	20	7
Verified and Acknowledged	/MAHMOUD DAHIMENE/ Examiner's Signature	Initials				

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**TITLE**

Method for manufacturing semiconductor device and substrate processing apparatus

FILING FEE RECEIVED 1830	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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